

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A plurality of integrated circuits, each of the integrated circuits comprising a processor and non-volatile memory and a tamper detection line arranged to obscure operation of the non-volatile memory, and including code for running identical software processes, wherein each of the integrated circuits also includes secret information used by the software process, the secret information in each chip being located in a different location in the memory relative to a plurality of the other chips.
2. (Original) A plurality of integrated circuits according to claim 1, wherein the code on each integrated circuit is such that the software process of each chip knows the location in memory via which the secret information is accessible.
3. (Original) A method of manufacturing a plurality of the integrated circuits of claim 2, including the steps of:
 - manufacturing a plurality of physical integrated circuits; and
 - injecting, into the non-volatile memory of each of the integrated circuits:
 - code for running a software process; and
 - secret information;
 - wherein the secret information is positioned in relatively different locations of the non-volatile memories and the code on each integrated circuit is such that the software process of each integrated circuit knows the location in memory via which the secret information is accessible on that integrated circuit.

REMARKS/ARGUMENTS

In response to the Examiner's Office Action of April 5, 2007 the Applicant respectfully submits the accompanying Amendment to the claims and the below Remarks.

Regarding Amendments

In the Amendments:

independent claim 1 is amended to specify that the integrated circuit comprises a tamper detection line which is arranged to obscure operation of the non-volatile memory. Support for this amendment can be found in paragraphs [8198]-[8223] of the present specification; and

dependent claims 2 and 3 are unchanged.

It is respectfully submitted that the above amendments do not add new matter to the present application.

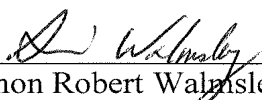
Regarding 35 USC 102(e) Rejections

It is respectfully submitted that the subject matter of above-described amended independent claim 1, and claims 2 and 3 dependent therefrom, is not disclosed by Gilbert et al. (US 7,165,177), because Gilbert merely discloses a non-volatile memory and does not further disclose or suggest arranging a tamper detection line to obscure the operation of the non-volatile memory, as is required by amended independent claim 1.


It is respectfully submitted that all of the Examiner's rejections have been traversed. Accordingly, it is submitted that the present application is in condition for allowance and reconsideration of the present application is respectfully requested.

Very respectfully,

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